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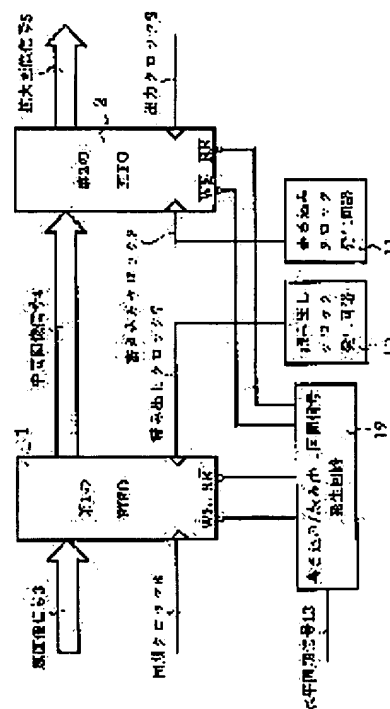
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(54) IMAGE ENLARGING DEVICE

(57)Abstract:

PURPOSE: To provide the image enlarging device enabling an enlarging operation with a comparatively low-speed FIFO memory and a clock generating circuit by operating a write clock to the FIFO memory less than the N-fold sampling frequency of a source image signal in the case of N-fold enlargement.

CONSTITUTION: The source image signal is written in a first FIFO memory 1, and an intermediate image signal is extracted from the first FIFO memory 1 at a frequency lower than the frequency of the source image signal. This extracted signal is written in a second FIFO memory 2 at a frequency lower than the N-fold frequency of the source image signal into the second FIFO memory 2 and read out of the second FIFO memory 2 at the frequency of the source image signal. Therefore, the element of a comparatively low operating frequency can be used for the second FIFO memory 2.



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